

**Amendment and Response**

Applicant: Claus Dworski et al.

Serial No.: 10/564,650

Filed: August 7, 2006

Docket No.: I431.144.101/FIN503PCT/US

Title: ELECTRICAL CIRCUIT AND METHOD FOR TESTING ELECTRONIC COMPONENT

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**REMARKS**

The following remarks are made in response to the Non-Final Office Action mailed march 16, 2010. Claims 46 and 54-64 have been withdrawn from consideration. Claims 43-45 have been allowed. Claims 39, 47 and 49-53 were rejected. Claims 33-37, 40-42 and 48 have been objected to. With this Response, claims 33, 35, 39, 40, 47-51 and 53 have been amended. Claims 33-37, 39-45 and 47-53 remain pending in the application and are presented for reconsideration and allowance.

**Drawings**

The Office Action objected to the drawings under 37 C.F.R. 1.83(a) for allegedly not showing every feature specified in the claims. The relevant claims have been amended such that the drawings show each feature of the claims. The objection to the drawings is thus believed to be overcome.

**Claim Objections**

Claims 33, 35, and 40 were objected to due to informalities. These claims have been amended along the lines suggested in the Office Action, thus overcoming the objections.

**Claim Rejections under 35 U.S.C. § 112**

Claims 39, 47, and 49-53 were rejected under 35 U.S.C. 112, second paragraph. These rejections are addressed as follows.

Claim 39 has been amended to more clearly recite the amplitude correction circuit, including an absolute value generator, an adder and a multiplier as illustrated, for example, in Figure 2 and described in the specification at page 12.

Claim 47 has been amended to remove the reference to the third input of the electrical circuit.

Claims 49-51 and 53 have all been amended to recite the electrical test circuit being tied to the electrical phase displacement circuit, and further to recite “the reference signal from the

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tester...”; to provide an antecedent for “the phase-displaced reference signal” and to recite the phase displacement circuit, referring to the phase displacement circuit of claim 40.

The rejections under section 112 are therefore believed to be overcome.

**Allowable Subject Matter**

Claims 43-45 were allowed, and claims 33-37, 39-42, and 47-53 would be allowable if the objections or rejections under 35 U.S.C. 112 were overcome. As noted above, all of the objections and rejections under section 112 are believed to be overcome. Therefore, all of the claims are believed to be in condition for allowance.

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**CONCLUSION**

In view of the above, Applicant respectfully submits that all of the pending claims are in form for allowance and are not taught or suggested by the cited references. Therefore, reconsideration and withdrawal of the rejections and allowance of the claims are respectfully requested.

No fees are required under 37 C.F.R. 1.16(h)(i). However, if such fees are required, the Patent Office is hereby authorized to charge Deposit Account No. 50-0471.

Please consider this a Petition for Extension of Time for a sufficient number of months to enter these papers, if appropriate. At any time during the pendency of this application, please charge any additional fees or credit overpayment to Deposit Account No. 500471.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed to Mark L. Gleason at Telephone No. (612) 767-2503, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

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Respectfully submitted,

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